UNIVERSITY OF SOUTHERN CALIFORNIA

COMPUTER ENGINEERING

SCREENING EXAMINATION

EE 536

MIXED-SIGNAL INTEGRATED SYSTEM DESIGN

SUGGESTED READING

1. REQUIRED: J. Choma and W-K. Chen, *Feedback Networks: Theory and Circuit Applications*. Singapore: World Scientific Press, 2007.

2. SUGGESTED READING

   "Lecture Aids," posted at www.jcatsc.com/EE536a.

   "Lecture Supplements," posted at www.jcatsc.com/EE536a.

   Thomas H. Lee, *The Design Of CMOS Radio–Frequency Integrated Circuits*. Cambridge, United Kingdom: Cambridge University Press, 2004.

   Phillip E. Allen and Douglas R. Holberg, *CMOS Analog Circuit Design*. New York: Oxford University Press, 2002.

   G. Palumbo and J. Choma, Jr., "An Overview Of Single And Dual Loop Analog Feedback; Part I: Basic Theory," *Journal of Analog Integrated Circuits And Signal Processing*, vol. 17, pp. 175-194, Nov. 1998.

   G. Palumbo and J. Choma, Jr., "An Overview Of Single And Dual Loop Analog Feedback; Part II: Design Examples," *Journal of Analog Integrated Circuits And Signal Processing*, vol. 17, pp. 195-219, Nov. 1998.

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Please be aware that these references are for guidance in BASIC knowledge. Ph.D. candidates are screened on the basis of talent, course knowledge, independent reading and experience.

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