

- Associate Professor, Electrical and Computer Engineering
- Ph.D. and M.S. in EE from UC Berkeley (2006/2002),  
B.S. in EE from National Taiwan University (1998)
- Colleen and Roberto Padovani Early Career Chair
- NSF CAREER Award (2014)
- DARPA Young Faculty Award (2014)
- Editorial Board member of SSC-L, TCAS-II, and Technical Program Committee of ISSCC, VLSI Symposium, CICC

**Research interests:**

- Data converter (ADC, DAC)
- Clock generation (PLL)
- Wireless/wireline communication circuits
- Low-power AI/ML computing circuitry
- Analog Circuit Design Methodology

