

Maysara HAMADA

Analog/RF IC Design Engineer

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An Electrical Engineering PhD student specialized in Analog/RF IC design.

PROFESSIONAL EXPERIENCE

July 2020 Present	Research Assistant, UNIVERSITY OF SOUTHERN CALIFORNIA, Los Angeles, CA <ul style="list-style-type: none">> Researching in time-based and nonuniform sampling ADC's> Participated in a 100-GHz ADC in FinFet technology
August 2019 February 2017	Teaching Assistant, CAIRO UNIVERSITY, Giza, Egypt Courses delivered : <ul style="list-style-type: none">> Circuits (Spring 2017, Fall 2017, Fall 2018, Spring 2019)> Control (Spring 2017, Spring 2018, Spring 2019)> Electronics (Spring 2018, Fall 2018)> Computer Architecture (Fall 2017)
August 2019 February 2018	Part-Time Analog Design Engineer, SI-WARE SYSTEMS, Cairo, Egypt <ul style="list-style-type: none">> Designed a 25-mA re-configurable LDO for a PA> Worked on a 5-GHz LC VCO (design and layout)> Worked on inductor modeling & characterization using EM simulation> Performed characterizations to several nanometer technology nodes> Performed a feasibility study and system-level simulations on SAW-less and mixer-first receivers
February 2018 August 2016	RF IC Design Researcher, ANALOG DEVICES INC., Cairo, Egypt <ul style="list-style-type: none">> Designed RF Predistorters for 5G Power Amplifiers> Contributed to system-level modeling of beamforming systems
August 2015 June 2015	Mixed-Signal IC Design Intern, OPTO-NANO-ELECTRONICS LAB, Cairo University, Giza, Egypt <ul style="list-style-type: none">> Researched in low-power time-based ADC's for biomedical applications> Verified published and novel topologies of PLL charge pumps and latched comparators

EDUCATION

May 2024 August 2019	PhD in Electrical Engineering, UNIVERSITY OF SOUTHERN CALIFORNIA, Los Angeles, CA GPA : 3.77
August 2019 September 2016	M.Sc. in Electronics Engineering, CAIRO UNIVERSITY, Giza, Egypt <ul style="list-style-type: none">> GPA : 3.8> Thesis : Low-Power RF Predistortion for Broadband Millimeter-Wave Power Amplifiers (sponsored by Analog Devices Inc.)
July 2016 September 2011	B.Sc. in Communications and Computer Engineering, CAIRO UNIVERSITY, Giza, Egypt <ul style="list-style-type: none">> Major : Electronics Engineering> GPA : 3.87> Graduation Project : Ultra Low-Power Wi-Fi HaLow Transceiver for IoT Applications (five award winner)

PUBLICATIONS

December 2019 Y. M. Abdelkader, M. M. Hamada and A. N. Mohieldin, "System Level Co-Simulation Approach for Ultra-Wideband Massive MIMO Beam Forming Phased Array Transmitters," 2019 31st International Conference on Microelectronics (ICM), Cairo, Egypt, 2019, pp. 78-81

SKILLS

Analog/RF Design	Cadence Virtuoso, Mentor Pyxis, Agilent ADS
Analog/RF Layout	Cadence Virtuoso, Mentor Pyxis (+ Calibre)
System-Level Modeling	VerilogA, Simulink, SystemVue, Agilent ADS
EM Simulation	Cadence EMX, Mentor IE3D, Sonnet
HDL	VHDL, Verilog
Coding & Scripting	MATLAB, C++, C#, SHELL (basic)

VOLUNTEERING WORK

June 2016	Electronics Director, IEEE CAIRO UNIVERSITY STUDENT BRANCH, Giza, Egypt
July 2015	

November 2014	Electronics Club Member, IEEE CAIRO UNIVERSITY STUDENT BRANCH, Giza, Egypt
June 2015	

CERTIFICATES

November 2018	GRE General Test Analytical Writing : 4/6 Quantitative Analysis : 169/170 Verbal Reasoning : 150/170
August 2018	TOEFL iBT Score : 114/120 Reading : 29/30 Listening : 27/30 Speaking : 28/30 Writing : 30/30

REFERENCES

Available upon request