

UNIVERSITY OF SOUTHERN CALIFORNIA

COMPUTER ENGINEERING

SCREENING EXAMINATION

**EE 477L**

## **MOS VLSI CIRCUIT DESIGN**

### SUGGESTED READING

1. CMOS Digital Integrated Circuits, 4th edition, Kang and Leblebici, McGraw-Hill.
2. Integrated Circuits: A Design Perspective, Jan Rabaey, Prentice Hall
3. Digital Integrated Circuit Design, Martin, Oxford
4. CMOS VLSI Design: A Circuits and Systems Perspective, Neil Weste and David Harris, 4<sup>th</sup> edition, Addison Wesley.

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Please be aware that these references are for guidance in BASIC knowledge. Ph.D. candidates are screened on the basis of talent, course knowledge, independent reading and experience.

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*Updated August 2023*