UNIVERSITY OF SOUTHERN CALIFORNIA
COMPUTER ENGINEERING
SCREENING EXAMINATION
EE 477L
MOS VLSI CIRCUIT DESIGN
SUGGESTED READING


2. Integrated Circuits: A Design Perspective, Jan Rabaey, Prentice Hall


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Please be aware that these references are for guidance in BASIC knowledge. Ph.D. candidates are screened on the basis of talent, course knowledge, independent reading and experience.

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