

UNIVERSITY OF SOUTHERN CALIFORNIA

COMPUTER ENGINEERING

SCREENING EXAMINATION

**EE 681**

## **COMPUTER AIDED DESIGN OF DIGITAL SYSTEMS II**

### SUGGESTED READING

1. Logic Synthesis and Verification Algorithms, by Prof. Hachtel and Prof. Somenzi, Kluwer.
2. Synthesis and Optimization of Digital Circuits, by Prof. Demicheli, McGraw-Hall

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Please be aware that these references are for guidance in BASIC knowledge. Ph.D. candidates are screened on the basis of talent, course knowledge, independent reading and experience.

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